

FORM PTO-1449	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. 108066-00097	SERIAL NO. TBD
LIST OF REFERENCES CITED BY APPLICANT <i>(Use several sheets if necessary)</i>		APPLICANT Watanu YOKOZEKI	
		FILING DATE August 26, 2003	GROUP

U.S. PATENT DOCUMENTS

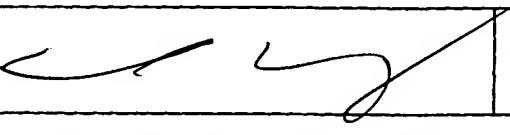
EXAMINER INITIAL		DOCUMENT NO.	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION YES NO PART.
MC	AG	2001-093275	4/6/2001	Japan			X
MC	AH	10-078836	3/24/1998	Japan			X
	AI						
	AJ						
	AK						
	AL						

OTHER REFERENCES (*Including Author, Title, Date, Pertinent Pages, Etc.*)

MC	AM	"Technology for a Low Power Consumption, High-Speed LSI" Realize Inc., pages 60-74
MC	AN	"A 512 Kbit low-voltage NV-SRAM with the size of a conventional SRAM" 2001 Symposium on VLSI Circuits Digest of Technical Papers
	AO	

EXAMINER		DATE CONSIDERED
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.